VII. ABSTRACT

A processor based computer system having dependency checking logic and a register
stack, wherein the system overrides the dependency logic such that move instructions associated
with the stack registers may be executed in parallel. The system operates such that it can be
determined whether a stack underflow exception has occurred and if it has, the move instructions
can be flushed, and a micro-code handler algorithm invoked that operates to allow execution of
the move instructions in parallel without a stack underflow exception.

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